

# mos field effect transistor $\mu PA622TT$

## N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

#### **DESCRIPTION**

The  $\mu \text{PA622TT}$  is a switching device which can be driven directly by a 4.0 V power source.

This device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

#### **FEATURES**

- 4.0 V drive available
- · Low on-state resistance

 $R_{DS(on)1} = 82 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = 10 \text{ V, ID} = 1.5 \text{ A)}$ 

 $R_{DS(on)2} = 120 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = 4.5 \text{ V, ID} = 1.0 \text{ A)}$ 

 $R_{DS(on)3} = 139 \text{ m}\Omega \text{ MAX.}$  (Vgs = 4.0 V, ID = 1.0 A)

### ORDERING INFORMATION

PART NUMBER	PACKAGE
μPA622TT	6 pin WSOF (1620)

Marking: WC

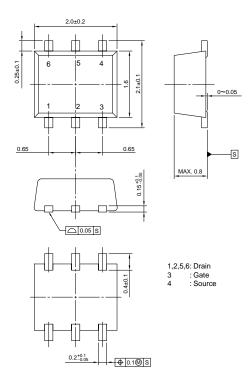
### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	30	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
Drain Current (DC) (T <sub>A</sub> = 25°C)	I <sub>D(DC)</sub>	±3.0	Α
Drain Current (pulse) Note1	ID(pulse)	±12	Α
Total Power Dissipation	P <sub>T1</sub>	0.2	W
Total Power Dissipation Note2	P <sub>T2</sub>	1.3	W
Channel Temperature	$T_ch$	150	°C
Storage Temperature	Tstg	-55 to +150	°C

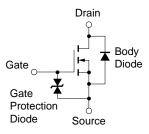
**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

**2.** Mounted on FR-4 board of 5000 mm<sup>2</sup> x 1.1 mm,  $t \le 5$  sec.

### PACKAGE DRAWING (Unit: mm)



#### **EQUIVALENT CIRCUIT**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

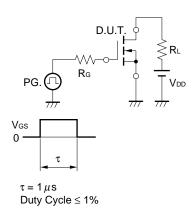
The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

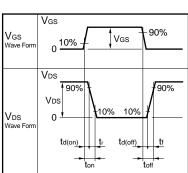


**ELECTRICAL CHARACTERISTICS (TA = 25°C)** 

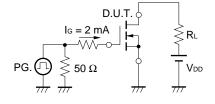
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Ipss	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			10	μΑ
Gate Leakage Current	Igss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.0 mA	1.5	2.0	2.5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.5 A	0.5	2.1		S
Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, ID = 1.5 A		65	82	mΩ
	RDS(on)2	Vgs = 4.5 V, ID = 1.0 A		90	120	mΩ
	RDS(on)3	Vgs = 4.0 V, ID = 1.0 A		104	139	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		155		pF
Output Capacitance	Coss	V <sub>G</sub> s = 0 V		45		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		27		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 15 V, I <sub>D</sub> = 1.5 A		10		ns
Rise Time	tr	Vgs = 10 V		28		ns
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 10 \Omega$		75		ns
Fall Time	<b>t</b> f			50		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = 24 V		3.8		nC
Gate to Source Charge	Qgs	Vgs = 10 V		0.7		nC
Gate to Drain Charge	Q <sub>GD</sub>	ID = 3.0 A		1.3		nC
Body Diode Forward Voltage	V <sub>F(S-D)</sub>	IF = 3.0 A, VGS = 0 V		0.90		V

### **TEST CIRCUIT 1 SWITCHING TIME**

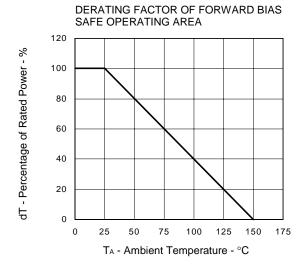


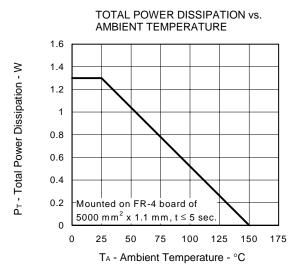


### **TEST CIRCUIT 2 GATE CHARGE**

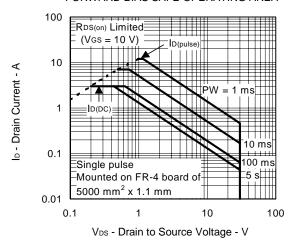


### TYPICAL CHARACTERISTICS (TA = 25°C)

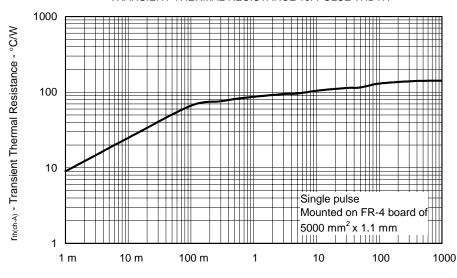




### FORWARD BIAS SAFE OPERATING AREA



### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



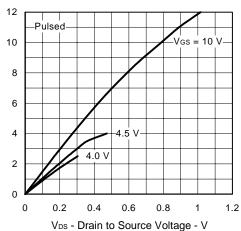
PW - Pulse Width - s

3

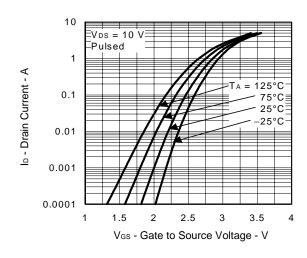
lo - Drain Current - A

VGS(off) - Gate Cut-off Voltage - V

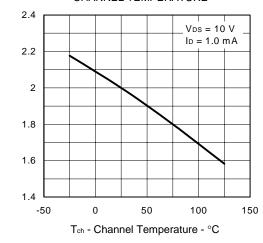
### DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



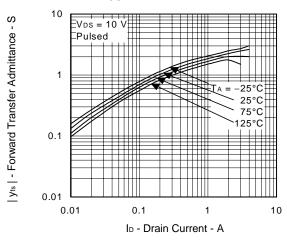
#### FORWARD TRANSFER CHARACTERISTICS



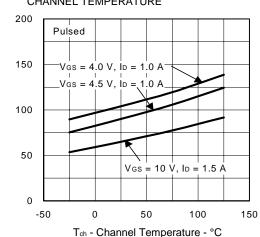
### GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



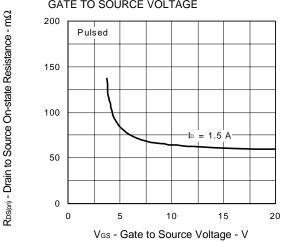
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



### DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



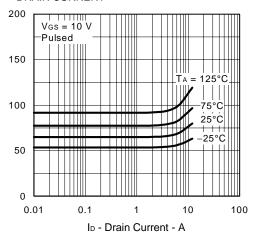
### DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



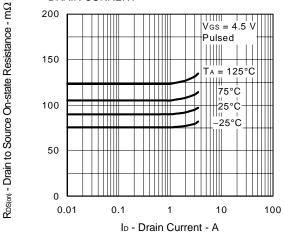
R<sub>DS(o1)</sub> - Drain to Source On-state Resistance - mΩ

RDS(m) - Drain to Source On-state Resistance - m\Omega

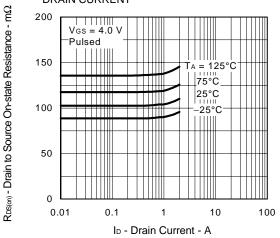
### DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



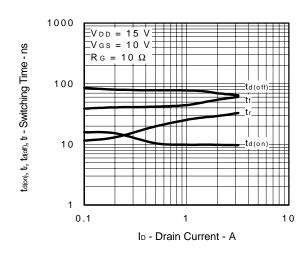
### DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



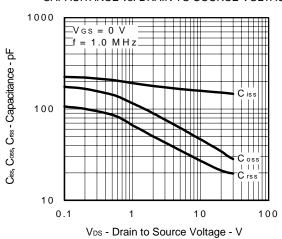
### DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



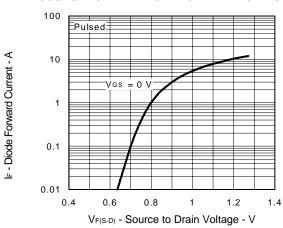
#### SWITCHING CHARACTERISTICS



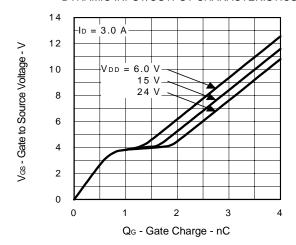
### CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



### SOURCE TO DRAIN DIODE FORWARD VOLTAGE



### DYNAMIC INPUT/OUTPUT CHARACTERISTICS



NEC  $\mu$ PA622TT

[MEMO]

- The information in this document is current as of September, 2002. The information is subject to change without notice. For actual design-in, refer to the latest publications of NEC's data sheets or data books, etc., for the most up-to-date specifications of NEC semiconductor products. Not all products and/or types are available in every country. Please check with an NEC sales representative for availability and additional information.
- No part of this document may be copied or reproduced in any form or by any means without prior written consent of NEC. NEC assumes no responsibility for any errors that may appear in this document.
- NEC does not assume any liability for infringement of patents, copyrights or other intellectual property rights of
  third parties by or arising from the use of NEC semiconductor products listed in this document or any other
  liability arising from the use of such products. No license, express, implied or otherwise, is granted under any
  patents, copyrights or other intellectual property rights of NEC or others.
- Descriptions of circuits, software and other related information in this document are provided for illustrative
  purposes in semiconductor product operation and application examples. The incorporation of these
  circuits, software and information in the design of customer's equipment shall be done under the full
  responsibility of customer. NEC assumes no responsibility for any losses incurred by customers or third
  parties arising from the use of these circuits, software and information.
- While NEC endeavours to enhance the quality, reliability and safety of NEC semiconductor products, customers
  agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize
  risks of damage to property or injury (including death) to persons arising from defects in NEC
  semiconductor products, customers must incorporate sufficient safety measures in their design, such as
  redundancy, fire-containment, and anti-failure features.
- NEC semiconductor products are classified into the following three quality grades:
  - "Standard", "Special" and "Specific". The "Specific" quality grade applies only to semiconductor products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of a semiconductor product depend on its quality grade, as indicated below. Customers must check the quality grade of each semiconductor product before using it in a particular application.
  - "Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots
  - "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)
  - "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC semiconductor products is "Standard" unless otherwise expressly specified in NEC's data sheets or data books, etc. If customers wish to use NEC semiconductor products in applications not intended by NEC, they must contact an NEC sales representative in advance to determine NEC's willingness to support a given application.

(Note)

- (1) "NEC" as used in this statement means NEC Corporation and also includes its majority-owned subsidiaries.
- (2) "NEC semiconductor products" means any semiconductor product developed or manufactured by or for NEC (as defined above).